

Status of the Claims

1. (currently amended) A method for executing a software application in a plurality of computers having respective hardware resources said hardware resources comprising a respective memory and a respective I/O device, wherein said computers include a first computer and a second computer that intercommunication over a network, said computers being operative to execute a virtual machine that runs under a guest operating system, comprising the steps of:

running at least a first virtual machine implementer and a second virtual machine implementer on said first computer and said second computer, using said respective memory, wherein said first and second virtual machine implementers run separately and independently of one another on said first and second computers, respectively; and

sharing said virtual machine between said first virtual machine implementer and said second virtual machine implementer using said respective I/O device in each of said first computer and said second computer to intercommunicate between said first computer and said second computer.

2. (previously presented) The method according to claim 1, further comprising the step of running said software application over said guest operating system, so that commands invoked by said software application are monitored or emulated by said first

virtual machine implementer and said second virtual machine implementer on said first computer and said second computer, while said hardware resources of said first computer and said second computer are shared by communication over said network.

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3. (original) The method according to claim 1, wherein at least one of said first virtual machine implementer and said second virtual machine implementer is a virtual machine monitor.

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4. (original) The method according to claim 1, wherein at least one of said first virtual machine implementer and said second virtual machine implementer is an emulator.

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5. (previously presented) The method according to claim 1, wherein at least said first computer comprises a first virtual node comprising a first physical CPU of said first computer and a second virtual node comprising a second physical CPU of said first computer.

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6. (original) The method according to claim 1, wherein said virtual machine comprises a first virtual machine and a second virtual machine, wherein said first virtual machine and said second virtual machine have a plurality of virtual CPU's that are virtualized by said first virtual machine implementer based on a first physical CPU and said second virtual machine implementer based on a second physical CPU, respectively.

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7. (original) The method according to claim 6, and a first virtual node comprises said first physical CPU and said second physical CPU.

5 8. (original) The method according to claim 7, wherein said first virtual machine implementer virtualizes at least one of said virtual CPU's of said first virtual machine based on said first physical CPU and virtualizes at least one of said virtual CPU's in said second virtual machine based on said second physical CPU.

10 9. (previously presented) The method according to claim 1, further comprising the steps of:

15 providing a management system for said first virtual machine implementer and said second virtual machine implementer to control said first computer and said second computer, respectively, wherein said management system comprises a wrapper for receiving calls to a device driver from said first virtual machine implementer, said wrapper invoking said device driver according to a requirement of said first virtual machine implementer.

20 10. (previously presented) The method according to claim 9, further comprising the step of providing a virtual PCI controller for said management system to control a physical PCI controller in one of said computers.

25 11. (previously presented) The method according to claim 9, further comprising the step of providing a virtual DMA controller

for said management system to control a physical DMA controller in one of said computers.

12. (previously presented) The method according to claim 11,
5 further comprising the steps of:

providing a virtual PCI controller to control a physical PCI controller in one of said computers; and

during a bootup phase of operation scanning a device list with said virtual PCI controller to identify devices having on-board
10 DMA controllers.

13. (previously presented) The method according to claim 1, further comprising the steps of:

with said first virtual machine implementer and said second
15 virtual machine implementer maintaining mirrors of a portion of said respective memory that is used by said guest operating system in each of said computers;

write-invalidating at least a portion of a page of said respective memory in one of said computers; and

20 transferring a valid copy of said portion of said page to said one computer from another of said computers via said network.

14. (currently amended) A computer software product, comprising a computer-readable medium in which computer program
25 instructions are stored, which instructions cause a plurality of computers having respective hardware resources, said hardware resources comprising a respective memory and a respective I/O device, to perform a method for executing a software application,

wherein said computers include a first computer and a second computer that intercommunicate over a network, and said computers being operative to execute a virtual machine that runs under a guest operating system, comprising the steps of:

- 5 running at least a first virtual machine implementer and a second virtual machine implementer on said first computer and said second computer, using said respective memory, wherein said first and second virtual machine implementers run separately and independently of one another on said first and second computers,
10 respectively; and
 sharing said virtual machine between said first virtual machine implementer and said second virtual machine implementer using said respective I/O device in each of said first computer and said second computer to intercommunicate between said first
15 computer and said second computer.

15. (original) The computer software product according to claim 14, wherein at least one of said first virtual machine implementer and said second virtual machine implementer is a
20 virtual machine monitor.

16. (original) The computer software product according to claim 14, wherein at least one of said first virtual machine implementer and said second virtual machine implementer is an
25 emulator.

17. (previously presented) The computer software product according to claim 14, wherein at least said first computer

comprises a first virtual node comprising a first physical CPU of said first computer and a second virtual node comprising a second physical CPU of said first computer.

5 18. (original) The computer software product according to claim 17, wherein said virtual machine comprises a first virtual machine and a second virtual machine, wherein said first virtual machine and said second virtual machine have a plurality of virtual CPU's that are virtualized by said first virtual machine
10 implementer based on said first physical CPU and said second virtual machine implementer based on said second physical CPU, respectively.

15 19. (original) The computer software product according to claim 18, wherein said plurality of virtual CPU's that are virtualized by said first virtual machine implementer based on said first physical CPU and said second virtual machine implementer based on said second physical CPU, respectively.

20 20. (original) The computer software product according to claim 18, wherein said first virtual node comprises said first physical CPU and said second physical CPU.

25 21. (original) The computer software product according to claim 20, wherein said first virtual machine implementer virtualizes at least one of said virtual CPU's of said first virtual machine based on said first physical CPU and virtualizes

at least one of said virtual CPU's in said second virtual machine based on said second physical CPU.

22. (previously presented) The computer software product
5 according to claim 14, wherein said computer is further instructed to perform the step of running said software application over said guest operating system, so that commands invoked by said software application are received by said first virtual machine implementer and said second virtual machine implementer on said first computer
10 and said second computer, while said hardware resources of said first computer and said second computer are shared by communication over said network.

23. (previously presented) The computer software product
15 according to claim 14, further comprising the steps of:

providing a management system for said first virtual machine implementer and said second virtual machine implementer to control said first computer and said second computer, respectively, wherein said management system comprises a wrapper for receiving
20 calls to a device driver from said first virtual machine implementer and said second virtual machine implementer, said wrapper invoking said device driver according to a requirement of said first virtual machine implementer and said second virtual machine implementer.

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24. (previously presented) The computer software product according to claim 23, further comprising the step of providing a

virtual PCI controller for said management system to control a physical PCI controller in one of said computers.

25. (previously presented) The computer software product
5 according to claim 23, wherein said computers are further instructed to perform the step of providing a virtual DMA controller for said management system to control a physical DMA controller in one of said computers.

10 26. (previously presented) The computer software product according to claim 25, wherein said computers are further instructed to perform the steps of:

providing a virtual PCI controller to control a physical PCI controller in one of said computers; and

15 during a bootup phase of operation scanning a device list with said virtual PCI controller to identify devices having on-board DMA controllers.

20 27. (previously presented) The computer software product according to claim 14, wherein said computers are further instructed to perform the steps of:

with said first virtual machine implementer and said second virtual machine implementer maintaining mirrors of a portion of said respective memory that is used by said guest operating system
25 in each of said computers;

write-invalidating at least a portion of a page of said respective memory in one of said computers; and

transferring a valid copy of said portion of said page to said one computer from another of said computers via said network.

28. (currently amended) A computer system for executing a software application, comprising:

a plurality of computers having respective hardware resources, said hardware resources comprising a respective memory and a respective I/O device, said computers comprising at least a first computer and a second computer;

a network connected to said first computer and said second computer providing intercommunication therebetween;

said first computer and said second computer being operative to execute a first virtual machine implementer and a second virtual machine implementer respectively using said respective memory, wherein a virtual machine is implemented concurrently by at least said first virtual machine implementer and said second virtual machine implementer, and wherein said first and second virtual machine implementers run separately and independently of one another on said first and second computers, respectively; and

said computers being operative to execute a guest operating system over said virtual machine, wherein said software application executes over said guest operating system, so that commands invoked by said software application are received by said first virtual machine implementer and said second virtual machine implementer on said first computer and said second computer, while said hardware resources of said first computer and said second computer are shared by communication over said network using said respective I/O device.

29. (original) The computer system according to claim 28, wherein said software application comprises a first software application and a second software application, said guest
5 operating system comprises a first guest operating system and a second guest operating system, and said virtual machine comprises a first virtual machine and a second virtual machine, wherein said first software application and said first guest operating system are associated with said first virtual machine, and said second
10 software application and said second guest operating system are associated with said second virtual machine.

30. (previously presented) The computer system according to claim 29, wherein one of said computers has a first physical CPU
15 and a second physical CPU, and said first virtual machine implementer virtualizes a first virtual CPU in said first virtual machine based on said first physical CPU and virtualizes a second virtual CPU in said second virtual machine based on said second physical CPU.

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31. (previously presented) The computer system according to claim 28, wherein at least said first computer comprises a first virtual node and a second virtual node.

25 32. (previously presented) The computer system according to claim 31, wherein said first computer comprises a first processor and a second processor, a first I/O device and a second I/O device, wherein said first I/O device is assigned to said first

processor, and said second I/O device is assigned to said second processor.

33. (previously presented) The computer system according to
5 claim 28, further comprising a minimal operating system executing
in each of said computers to invoke said first virtual machine
implementer and said second virtual machine implementer so that
said first virtual machine implementer and said second virtual
machine implementer control said computers.

10 34. (previously presented) The computer system according to
claim 28, further comprising a management system for said first
virtual machine implementer and said second virtual machine
implementer to control said first computer and said second
15 computer, respectively, wherein said management system comprises a
wrapper for receiving calls to a device driver from said first
virtual machine implementer and said second virtual machine
implementer, said wrapper invoking said device driver according to
a requirement of said first virtual machine implementer and said
20 second virtual machine implementer.

35. (previously presented) The computer system according to
claim 34, further comprising a virtual PCI controller for said
management system to control a physical PCI controller in one of
25 said computers.

36. (previously presented) The computer system according to
claim 34, further comprising a virtual DMA controller for said

management system to control a physical DMA controller in one of said computers.

37. (previously presented) The computer system according to
5 claim 28, further comprising a memory management system that
maintains mirrors of a portion of said respective memory that is
used by said guest operating system in each of said computers,
wherein said memory management system write-invalidates at least a
portion of a page of said respective memory in one of said
10 computers; and transfers a valid copy of said portion of said page
to said one computer from another of said computers via said
network.

38. (previously presented) The method according to claim 1,
15 wherein said guest operating system consists of exactly one
instance of a single guest operating system.

39. (previously presented) The computer software product
according to claim 14, wherein said guest operating system
20 consists of exactly one instance of a single guest operating
system.

40. (previously presented) The method according to claim 1,
wherein said first virtual machine implementer and said second
25 virtual machine implementer are operative to present said
respective memory of said first computer and said respective
memory of said second computer as a single shared memory to said
guest operating system, the method further comprising the step of

distributing instructions of said guest operating system to said single shared memory.

41. (previously presented) The computer software product
5 according to claim 14, wherein said first virtual machine
implementer and said second virtual machine implementer are
operative to present said respective memory of said first computer
and said respective memory of said second computer as a single
shared memory to said guest operating system, and to distribute
10 instructions of said guest operating system to said single shared
memory.

42. (previously presented) The computer system according to
claim 28, wherein said first virtual machine implementer and said
15 second virtual machine implementer are operative to present said
respective memory of said first computer and said respective
memory of said second computer as a single shared memory to said
guest operating system, and to distribute instructions of said
guest operating system to said single shared memory.

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43. (new) The method according to claim 1, wherein said
first and second computers comprise separate, respective first and
second central processing units (CPUs), first and second memories,
first and second I/O devices, and first and second buses that
25 respectively interconnect the first CPU with the first I/O device
and the second CPU with the second I/O device.

44. (new) The computer software product according to claim 14, wherein said first and second computers comprise separate, respective first and second central processing units (CPUs), first and second memories, first and second I/O devices, and first and second buses that respectively interconnect the first CPU with the first I/O device and the second CPU with the second I/O device.

45. (new) The computer system according to claim 28, wherein said first and second computers comprise separate, respective first and second central processing units (CPUs), first and second memories, first and second I/O devices, and first and second buses that respectively interconnect the first CPU with the first I/O device and the second CPU with the second I/O device.